



Specifications for bit error module

- Measures actual bit errors while a known PRBS-11 pseudo-random test sequence is being transmitted.
- Accurate BER measurement down to 10^{-6}
- Adjustable measurement window from 1,000 bits to 1,000,000,000 bits, to trade off BER range and measurement duration
- Fast automatic synchronization
- Cycle slips detection
- 32-bit cumulative BER counter for long duration measurements
- 1 bit serial / 2 bit parallel input selection (I before Q, or I/Q)

- ComScope Software : Key internal signals can be captured in real-time and displayed on host computer itself. BER measurement is made by counting actual errors in the received bit stream. The received bit stream is compared with a locally generated replica of the reference PRBS-11 sequence. The reference sequence is a periodic 2047-bit long maximum length sequence generated by a 11-tap linear feedback shift register.

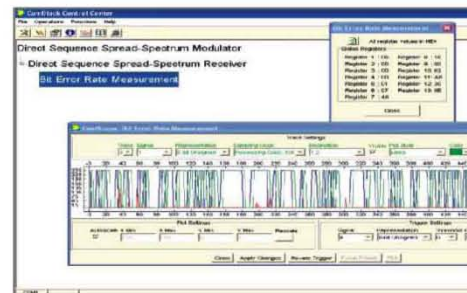
- Power Supply :** 220 V $\pm 10\%$, 50 Hz / 60 Hz on request

- Power Consumption :** 2.5 VA (approx.)

- Dimensions (mm.) :** W 255 \times D 155 \times H 80

Extra Experiments with ST2131A

- Spreading-despreading with onboard NRZ binary data (Data 1 & Data 2 (inverted) bits) as a data message.
- Bit error rate measurement with PRBS-11 data (2047 bits) & signal capture on ComScope Software.
- Dimensions (mm.) :** W 440 \times D 262 \times H 154



Software Window

